Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Presently Amended) A method for retaining error-control code protection across block-size discontinuities occurring between incoming information, having incoming data formatted into ingress data blocks and ingress headers, one ingress header associated with each ingress data block and conveying information about the each ingress data block, and outgoing information having the incoming data reformatted into egress data blocks with sizes different from the ingress data blocks and egress headers, one egress header associated with each egress data block and conveying information about the each egress data block, the method comprising:

   (a) generating a code check from data in an ingress data block and from an ingress header associated with the ingress data block;

   (b) generating a code check from the code check generated in step (a) and ~~an~~ a new egress header associated with an egress data block ~~derived~~ containing data from the ingress data block but having a different block size; and

   (c) generating the outgoing information by combining the egress header with the associated egress data block and code check generated in step (b).

2. (Previously amended) The method of claim 1 wherein step (b) comprises:

   (b1) generating a code check from the egress header;

   (b2) generating a code check by subtracting a portion of the code check generated from the associated ingress header in step (a) from the code check generated in step (b1); and

(b3) adding the code check generated in step (a) to the code check generated in step (b2).

3. (Original) The method of claim 2 wherein step (b2) comprises adding the inverse of the portion of the code check generated from the ingress header in step (a) to the code check generated in step (b1).

4. (Presently Amended) The method of claim 1 wherein step (a) further comprises:

(a1) ~~modifying~~ rotating the code check generated from data in the ingress data block and the associated ingress header ~~to compensate for~~ by an amount equal to the number of non-data bits added to ~~the~~ a previous ingress data block.

5. Canceled.

6. (Original) The method of claim 1 wherein step (a) further comprises:

(a2) modifying the incoming information to compensate for non-data bits added to the ingress data block.

7. (Original) The method of claim 1 wherein step (c) comprises concatenating the egress header with the associated egress data block and the code check generated in step (b).

8. (Previously Amended) The method of claim 1 wherein step (a) comprises generating the code check as a one's-complement sum of successive *n*-bit binary words included in the ingress data block and the associated ingress header.

9. (Previously Amended) The method of claim 1 wherein step (b) comprises generating the check code as a one's-complement sum of successive *n*-bit binary words included in the egress header.

10. (Previously Amended) The method of claim 1 wherein step (a) comprises generating the check code as a term-by-term modulo-two sum of successive *n*-bit binary words included in the ingress data block and the associated ingress header.

11. (Previously Amended) The method of claim 1 wherein step (b) comprises generating the code check as a term-by-term modulo-two sum of successive *n*-bit binary words included in the egress header.

12. (Previously Amended) The method of claim 1 wherein step (a) comprises generating the code check as the residue of the ingress data block and the associated ingress header modulo a generator polynomial.

13. (Previously Amended) The method of claim 1 wherein step (b) comprises generating the code check as the residue of the egress data block modulo a generator polynomial.

14. (Original) The method of claim 1 wherein the incoming information includes an incoming code check associated with each ingress data block and step (a) further comprises comparing the ingress code check to the incoming code check and generating an error when the ingress code check does not equal the incoming code check.

15. (Presently Amended) Apparatus for retaining error-control code protection across block-size discontinuities occurring between incoming information, having incoming data formatted into ingress data blocks and ingress headers, one ingress header associated with each ingress data block and conveying

information about the each ingress data block, and outgoing information having the incoming data reformatted into egress data blocks with sizes different from the ingress data blocks and egress headers, one egress header associated with each egress data block and conveying information about the each egress data block, the apparatus comprising:

an ingress encoder that generates an ingress code check from data in an ingress data block and from an ingress header associated with the ingress data block;

an egress encoder that generates a egress code check from ~~an~~ (1) a new egress header associated with an egress data block ~~derived~~ containing data from the ingress data block but having a different block size and ~~from~~ (2) the ingress code check; and

a multiplexer that generates the outgoing information by combining the egress header with the associated egress data block and the egress code check.

16. (Previously Amended) The apparatus of claim 15 wherein the egress encoder comprises:

an outgoing encoder that generates an egress code check from the egress header and from intermediate contents of the outgoing encoder;

a controller that subtracts a portion of the ingress code check generated from the associated ingress header from the outgoing encoder intermediate contents and adds the ingress code check to the outgoing encoder intermediate contents.

17. (Original) The apparatus of claim 16 wherein the controller further comprises a mechanism that modifies the ingress code check to compensate for non-data bits added to the ingress data block.

18. (Previously Amended) The apparatus of claim 16 wherein the controller comprises a mechanism that rotates the bits of the ingress code check to compensate for non-data bits added to the ingress data block.

19. (Original) The apparatus of claim 16 wherein the controller adds the inverse of the portion of the code check generated from the ingress header by the ingress encoder to the outgoing encoder contents.

20. (Original) The apparatus of claim 15 further comprising a mechanism that modifies the incoming information to compensate for non-data bits added to the ingress data block.

21. (Original) The apparatus of claim 15 wherein the multiplexer comprises a mechanism that concatenates the egress header with then associated egress data block and the egress code check.

22. (Original) The apparatus of claim 15 wherein the ingress encoder comprises a one's-complement encoder that generates a one's-complement sum of successive *n*-bit binary words included in the ingress data block and the associated ingress header.

23. (Original) The apparatus of claim 15 wherein the outgoing encoder comprises a one's-complement encoder that generates a one's-complement sum of successive *n*-bit binary words included in the egress header.

24. (Original) The apparatus of claim 15 wherein the ingress encoder comprises a vertical-parity-check encoder that generates a term-by-term modulo-two sum of successive *n*-bit binary words included in the ingress data block and the associated ingress header.

25. (Original) The apparatus of claim 15 wherein the outgoing encoder comprises a vertical-parity-check encoder that generates a term-by-term modulo-two sum of successive *n*-bit binary words included in the egress header.

26. (Original) The apparatus of claim 15 wherein the ingress encoder comprises a cyclic-residue-code encoder that generates the residue of the ingress data block and the associated ingress header modulo a generator polynomial.

27. (Original) The apparatus of claim 15 wherein the outgoing encoder comprises a vertical-parity-check encoder that generates the residue of the egress data block modulo a generator polynomial.

28. (Original) The apparatus of claim 15 wherein the incoming information includes an incoming code check associated with each ingress data block and the apparatus further comprises a comparator for comparing the ingress code check to the incoming code check and generating an error when the ingress code check does not equal the incoming code check.

29. (Presently Amended) A computer program product for retaining error-control code protection across block-size discontinuities occurring between incoming information, having incoming data formatted into ingress data blocks and ingress headers, one ingress header associated with each ingress data block and conveying information about the each ingress data block, and outgoing information having the incoming data reformatted into egress data blocks with sizes different from the ingress data blocks and egress headers, one egress header associated with each egress data block and conveying information about the each egress data block, the computer program product comprising a computer usable medium having computer readable program code thereon, including:

program code that generates an ingress code check from data in an ingress data block and from an ingress header associated with the ingress data block;

program code that generates a egress code check from (1) the ingress code check and ~~an~~ (2) a new egress header associated with an egress data block ~~derived~~ containing data from the ingress data block but having a different block size; and

program code that generates the outgoing information by combining the egress header with the associated egress data block and the egress code check.

30. (Previously Amended) The computer program product of claim 29 wherein the program code that generates an egress code check comprises:

program code that generates an egress code check from the egress header;

program code that subtracts a portion of the ingress code check generated from the associated ingress header from the egress code check; and

program code that adds the ingress code check to the egress code check.

31. (Original) The computer program product of claim 30 wherein the program code that subtracts a portion of the ingress code check from the egress code check comprises program code that adds the inverse of the portion of the ingress code check to the egress code check generated.

32. (Presently Amended) The computer program product of claim 29 wherein the program code that generates an ingress code check further comprises program code that ~~modifies~~ rotates the ingress code check ~~to compensate for~~ by an amount equal to the number of non-data bits added to ~~the~~ a previous ingress data block.

33. Canceled.

34. (Original) The computer program product of claim 29 wherein the program code that computes the ingress code check further comprises program code that modifies the incoming information to compensate for non-data bits added to the ingress data block.

35. (Original) The computer program product of claim 29 wherein the program code that generates the outgoing information comprises program code that concatenates the egress header with the associated egress data block and the egress code check.

36. (Original) The computer program product of claim 29 wherein the program code that generates the ingress code check comprises program code that generates a one's-complement sum of successive *n*-bit binary words included in the ingress data block and the associated ingress header.

37. (Original) The computer program product of claim 29 wherein the program code that generates the egress code check comprises program code that generates a one's-complement sum of successive *n*-bit binary words included in the egress header.

38. (Original) The computer program product of claim 29 wherein the program code that generates the ingress code check comprises program code that generates a term-by-term modulo-two sum of successive *n*-bit binary words included in the ingress data block and the associated ingress header.

39. (Original) The computer program product of claim 29 wherein the program code that generates the egress code check comprises program code that generates a term-by-term modulo-two sum of successive *n*-bit binary words included in the egress header.

40. (Original) The computer program product of claim 29 wherein the program code that generates the ingress code check comprises program code that generates the residue of the ingress data block and the associated ingress header modulo a generator polynomial.

41. (Original) The computer program product of claim 29 wherein the program code that generates the egress code check comprises program code that generates the residue of the egress data block modulo a generator polynomial.

42. (Original) The computer program product of claim 29 wherein the incoming information includes an incoming code check associated with each ingress data block and wherein the program code that generates the ingress code check further comprises program code that compares the ingress code check to the incoming code check and generates an error when the ingress code check does not equal the incoming code check.

43. (Previously Amended) A computer data signal embodied in a carrier wave for retaining error-control code protection across block-size discontinuities occurring between incoming information, having incoming data formatted into ingress data blocks and ingress headers, one ingress header associated with each ingress data block and conveying information about the each ingress data block, and outgoing information having the incoming data reformatted into egress data blocks with sizes different from the ingress data blocks and egress headers, one egress header associated with each egress data block and conveying information about the each egress data block, the computer data signal comprising:

program code that generates an ingress code check from data in an ingress data block and from an ingress header associated with the ingress data block;

program code that generates a egress code check from (1) the ingress code check and ~~an~~ (2) a new egress header associated with an egress data block ~~derived~~ containing data from the ingress data block but having a different block size; and

program code that generates the outgoing information by combining the egress header with the associated egress data block and the egress code check.